

**3RD SEMESTER EE TIME TABLE
DEPARTMENT OF ELECTRICAL ENGINEERING**

w.e.f. 09.07.2024

Room No. 113 (Block-I)

Days	9:00 to 9:55	10:00 to 10:55	11:05 to 12:00	12:05 to 1:00	1:00 to 2:30	2:30 to 3:20	3:20 to 4:10	4:10 to 5:00
MON	EM-I (SC)	MATH-III (SM)	A&DEC (M Pal)	ECA (MKS)	B R E A K	ACC-I Lab GR-I (CSE)/ A&DEC Lab GR-II (M Pal & DM)		
TUES	EM-I (SC)	A&DEC (M Pal)	MATH-III (SM)	EE (BSH)		ECA Lab GR-I (MKS & M Panda)/ EM-I Lab GR-II (SC & MKS)		
WED	ECA (MKS)	ACC-I (CSE)	EM-I (SC)	EE (BSH)		EM-I Lab GR-I (SC & MKS)		
THU	ECA (MKS)	A&DEC (M Pal)	ACC-I (CSE)	MATH-III (SM)		ACC-I Lab Gr-II (CSE)		
FRI	EE (BSH)	ACC-I (CSE)	A&DEC (M Pal)	EM-I (SC)		ECA Lab GR-II (MKS & M Panda)/A&DEC Lab Gr-I (M Pal & DM)		
SAT	MATH-III (SM)	ACC-I (CSE)	ECA (MKS)	xxx		xxx		

Course Code	Course Title	Name of the Faculty
HSBSS2001	Mathematics-III (Math-III)	BSH Dept.
	Advance Competency Course-1	CSE Dept.
HSHS2001	Engineering Economics(EE)	BSH Dept.
EOPC2002	Analog & Digital Electronic Circuits(A&DEC)	Dr. Madhumita Pal
EEPC2001	Electrical Circuit Analysis(ECA)	Dr. Manoj Kumar Senapati
EEPC2002	Electrical Machinse-I	Mrs. Sasmita Choudhury

**Time Table Coordinator
Mr. Rakesh Rajan Shukla**

**HOD EE
Dr. Sangram Keshori Mohapatra**